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**JUN 10 2005**

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**FROM:** Robert A. Saltzberg, Reg. No. 36,910**DATE:**6/10/05

Number of pages with cover page:	14	Originals Will Not Follow
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**Comments:**

Attorney Docket No.:333772000800  
Group Art Unit: 2829  
Examiner: Not Assigned  
Application No.: 10/772,434  
Filed: February 6, 2004  
Title: METHOD AND STRUCTURE TO DEVELOP A TEST PROGRAM FOR  
SEMICONDUCTOR INTEGRATED CIRCUITS  
Inventor: KRISHNASWAMY et al.

**Enclosed are the following documents:**

1. Transmittal, 1 page;
2. Request for Corrected Filing Receipt, 2 pages;
3. Copy of Notice of Recordation of Assignment, 2 pages;
4. Copy of Foreign Filing License, 1 page;
5. Marked up COPY of official filing receipt, 2 pages;
6. Supplemental Application Data Sheet, 5 pages.

**Comments: Please acknowledge receipt.**

PTO/SB/21 (09-04)

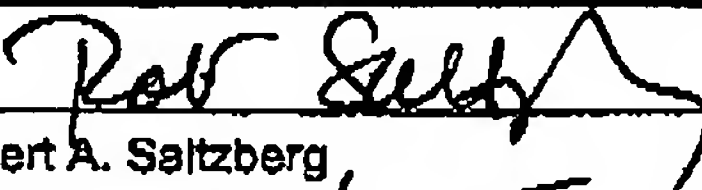
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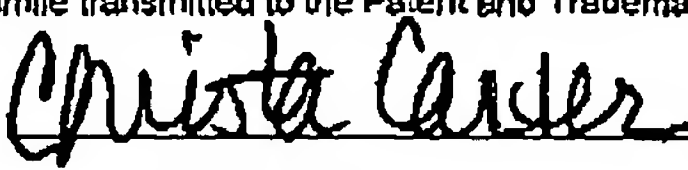
U.S. Patent and Trademark Office; U.S. DEPARTMENT OF COMMERCE

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<b>TRANSMITTAL FORM</b>  <i>(to be used for all correspondence after initial filing)</i>	Application Number	10/772,434
	Filing Date	February 6, 2004
	First Named Inventor	Ramachandran KRISHNASWAMY
	Art Unit	2829
	Examiner Name	Not Yet Assigned
Total Number of Pages In This Submission	13 <del>12</del>	Attorney Docket Number 333772000800

ENCLOSURES (Check all that apply)		
<input type="checkbox"/> Fee Transmittal Form <input type="checkbox"/> Fee Attached <input type="checkbox"/> Amendment/Reply <input type="checkbox"/> After Final <input type="checkbox"/> Affidavits/declaration(s) <input type="checkbox"/> Extension of Time Request <input type="checkbox"/> Express Abandonment Request <input type="checkbox"/> Information Disclosure Statement <input type="checkbox"/> Certified Copy of Priority Document(s) <input type="checkbox"/> Reply to Missing Parts/Incomplete Application <input type="checkbox"/> Reply to Missing Parts under 37 CFR 1.52 or 1.53	<input type="checkbox"/> Drawing(s) <input type="checkbox"/> Licensing-related Papers <input type="checkbox"/> Petition <input type="checkbox"/> Petition to Convert to a Provisional Application <input type="checkbox"/> Power of Attorney, Revocation Change of Correspondence Address <input type="checkbox"/> Terminal Disclaimer <input type="checkbox"/> Request for Refund <input type="checkbox"/> CD, Number of CD(s) _____ <div style="border: 1px solid black; width: 100px; height: 50px; margin: 5px auto;"></div> Landscape Table on CD	<input type="checkbox"/> After Allowance Communication to TC <input type="checkbox"/> Appeal Communication to Board of Appeals and Interferences <input type="checkbox"/> Appeal Communication to TC (Appeal Notice, Brief, Reply Brief) <input type="checkbox"/> Proprietary Information <input type="checkbox"/> Status Letter <input checked="" type="checkbox"/> Other Enclosure(s) (please identify below): <ul style="list-style-type: none"> <li>* Request for Corrected Official Filing Receipt, 2 pages</li> <li>* Copy of Notice of Recordation of Assignment, 2 pages</li> <li>* Copy of Foreign Filing License, 1 page</li> <li>* Marked up COPY of Official Filing Receipt, 2 pages</li> <li>* Supplemental Application Data Sheet, 5 pages</li> <li>* Fax Cover Sheet</li> </ul>
Remarks Facsimile cover sheet is the uncounted page in this submission.		

SIGNATURE OF APPLICANT, ATTORNEY, OR AGENT			
Firm Name	MORRISON & FOERSTER LLP (Customer No. 20872)		
Signature			
Printed name	Robert A. Saltzberg		
Date	6/9/05	Reg. No.	36,910

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Dated: 6/10/05	Signature:  (Christa Carter)

sf-1843470

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Dated: 6/10/05Signature: Christa Carter

(Christa Carter)

Docket No.: 333772000800  
(PATENT)**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In re Patent Application of:  
Ramachandran KRISHNASWAMY et al.

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Application No.: 10/772,434

Confirmation No.: 5995 JUN 10 2005

Filed: February 6, 2004

Art Unit: 2829

For: METHOD AND STRUCTURE TO DEVELOP  
A TEST PROGRAM FOR SEMICONDUCTOR  
INTEGRATED CIRCUITS

Examiner: Not Yet Assigned

**REQUEST FOR CORRECTED FILING RECEIPT**

Filing Receipt Corrections  
Office of Initial Patent Examination  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Dear Sir:

Applicant hereby requests that a corrected Filing Receipt be issued in the above-identified patent application. The official Filing Receipt received by Applicant, a marked-up copy of which is attached hereto, has the following errors, as indicated on the marked-up copy.

Under the heading "Assignment for Published Patent Application", please replace "Santa Clara, CA" with --Tokyo, Japan--. A copy of the Notice of Recordation of Assignment is attached hereto.

Under the heading "If Required, Foreign Filing License Granted:" please replace "05/05/2004" with --02/06/2004--. A copy of the Foreign Filing License is attached hereto.

sf-1843505

Application No.: 10/772,434

2

Docket No.: 333772000800

The mistake occurred in the printing of the Filing Receipt, therefore it is not believed that any fee is required. However, if this is not the case, the Assistant Commissioner is hereby authorized to charge the required fee to Deposit Account No. 03-1952. Acceptance of this Request for Corrected Filing Receipt is respectfully requested.

Applicant additionally requests that all pertinent U.S. Patent and Trademark Office records relating to the subject application be changed to reflect this correction.

In the unlikely event that the transmittal letter is separated from this document and/or the Patent Office determines that an extension and/or other relief is required, Applicant petitions for any required relief including extensions of time and authorizes the Commissioner to charge the cost of such petitions and/or other fees due in connection with the filing of this document to Deposit Account No. 03-1952 referencing docket no. 333772000800.

Dated: 6/9/05

Respectfully submitted,

By Robert A. Saltzberg  
Robert A. Saltzberg  
Registration No.: 36,910  
MORRISON & FOERSTER LLP  
425 Market Street  
San Francisco, California 94105  
(415) 268-6428

sf-1843505

**CORRECTED  
NOTICE****UNITED STATES PATENT AND TRADEMARK OFFICE**UNDER SECRETARY OF COMMERCE FOR INTELLECTUAL PROPERTY AND  
DIRECTOR OF THE UNITED STATES PATENT AND TRADEMARK OFFICE

MARCH 18, 2005

PTAS

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MORRISON | FOERSTER****UNITED STATES PATENT AND TRADEMARK OFFICE  
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RECORDATION DATE: 06/30/2004

REEL/FRAME: 014807/0672  
NUMBER OF PAGES: 3

BRIEF: ASSIGNMENT OF ASSIGNOR'S INTEREST (SEE DOCUMENT FOR DETAILS).

ASSIGNOR:

KRISHNASWAMY, RAMACHANDRAN

DOC DATE: 05/27/2004

ASSIGNOR:

SINGH, HARSANJEET

DOC DATE: 05/27/2004

ASSIGNOR:

PRAMANICK, ANKAN

DOC DATE: 06/01/2004

ASSIGNOR:

BLSTON, MARK

DOC DATE: 06/01/2004

ASSIGNOR:

CHEN, LEON

DOC DATE: 05/22/2004

ASSIGNOR:

ADACHI, TOSHIAKI

DOC DATE: 05/27/2004

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014807/0672 PAGE 2

ASSIGNOR:  
TAHARA, YOSHIHUMI

DOC DATE: 05/27/2004

ASSIGNEE:  
ADVANTEST CORPORATION  
1-32-1 ASAHI-CHO  
NERIMA-KU  
TOKYO, JAPAN 179-0071

SERIAL NUMBER: 10772434

FILING DATE: 02/06/2004

PATENT NUMBER:

ISSUE DATE:

TITLE: METHOD AND STRUCTURE TO DEVELOP A TEST PROGRAM FOR SEMICONDUCTOR  
INTEGRATED CIRCUITS

MARGARET LASALLE, PARALEGAL  
ASSIGNMENT DIVISION  
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COMMISSIONER FOR PATENTS  
UNITED STATES PATENT AND TRADEMARK OFFICE  
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SERIAL NUMBER	REQUEST DATE	FIRST NAMED APPLICANT	ATTORNEY DOCKET NO.
P-108,530	2/6/04	RAMACHANDRAN KRISHNASWAMY	333772000800

Title:  
**METHOD AND STRUCTURE TO DEVELOP A TEST PROGRAM  
FOR SEMICONDUCTOR INTEGRATED CIRCUITS**

Art Unit	Paper Number

**Correspondence Address:**

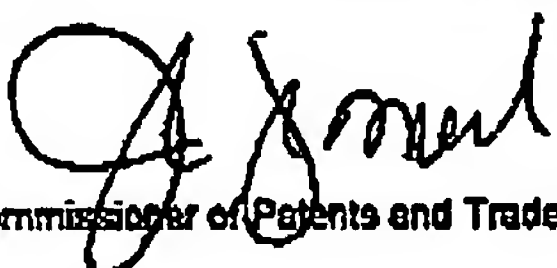
**ROBERT A. SALTZBERG  
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License Number: 531,982

Grant Date: 06-Feb-04

Approved:

  
for Commissioner of Patents and Trademarks

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[Title 35, United States Code (1952) Sections 184, 185, 186]

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APPL NO.	FILING OR 371 (c) DATE	ART UNIT	FIL FEE REC'D	ATTY. DOCKET NO	DRAWINGS	TOT CLMS	IND CLMS
10/772,434	02/08/2004	2829	954	333772000800	9	23	1

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20872  
MORRISON & FOERSTER LLP  
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SAN FRANCISCO, CA 94105-2482

JUL 29 2004

MORRISON &amp; FOERSTER, LLP

CONFIRMATION NO. 5995

UPDATED FILING RECEIPT

\*OC000000013327065\*

\*OC000000013327065\*

Date Mailed: 07/26/2004

Receipt is acknowledged of this regular Patent Application. It will be considered in its order and you will be notified as to the results of the examination. Be sure to provide the U.S. APPLICATION NUMBER, FILING DATE, NAME OF APPLICANT, and TITLE OF INVENTION when inquiring about this application. Fees transmitted by check or draft are subject to collection. Please verify the accuracy of the data presented on this receipt. If an error is noted on this Filing Receipt, please write to the Office of Initial Patent Examination's Filing Receipt Corrections, facsimile number 703-746-9195. Please provide a copy of this Filing Receipt with the changes noted thereon. If you received a "Notice to File Missing Parts" for this application, please submit any corrections to this Filing Receipt with your reply to the Notice. When the USPTO processes the reply to the Notice, the USPTO will generate another Filing Receipt incorporating the requested corrections (if appropriate).

## Applicant(s)

Ramachandran Krishnaswamy, Santa Clara, CA;  
Harsanjeet Singh, Santa Clara, CA;  
Ankan Pramanick, Santa Clara, CA;  
Mark Elston, Santa Clara, CA;  
Leon Chen, Santa Clara, CA;  
Toshiaki Adachi, Santa Clara, CA;  
Yoshihumi Tahara, Santa Clara, CA;

## Assignment For Published Patent Application

Advantest Corporation, ~~Santa Clara, CA;~~ **TOKYO, JAPAN**

## Domestic Priority data as claimed by applicant

This appln claims benefit of 60/447,839 02/14/2003  
and claims benefit of 60/449,622 02/24/2003

## Foreign Applications

If Required, Foreign Filing License Granted: ~~05/04/2004~~ **2/6/2004**

Projected Publication Date: 11/04/2004

Non-Publication Request: No

Early Publication Request: No

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**Title**

Method and structure to develop a test program for semiconductor integrated circuits

**Preliminary Class**

324

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Title 37, Code of Federal Regulations, 5.11 & 5.15**

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